

Basic contents

1). Scope of Application

This specification shall be applied for the Ferrite cores to be delivered to Messrs. LEPCOS ST.PETERSB

2). Name of product

The name of the product to be defined in this specification shall be defined as PC47UU100x151x30

3). Related Specifications

4). Description

Items	Attached drawings and tables	Page
Outer appearance structure and size		2
Electrical characteristics		2
Marking		2
Manufacturing site		2
Others		3

Revision Records

Edition	Date	Revised by	Revision
00	2018/5/4	Huoyang Lin	First Issue

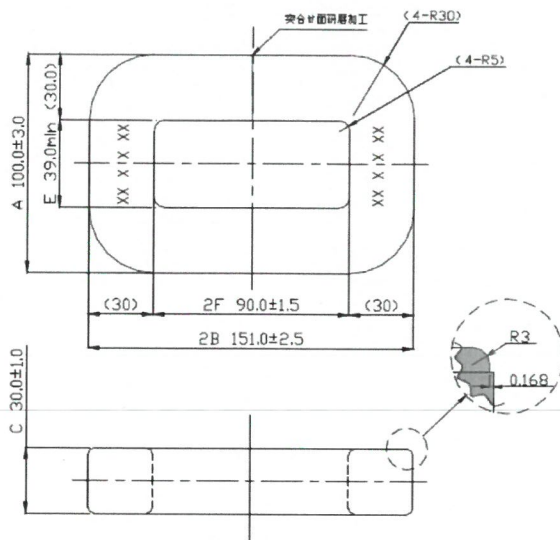
Magnetics Business Group

Specification No.

XL-0911-2647 (00)

TDK CORPORATION

1. Dimensions (Unit : mm)



Sym	Dimension (mm)
A	100.00 ± 3.00
2B	151.00 ± 2.50
C	30.00 ± 1.00
E	39.00 MIN
2F	90.00 ± 1.50

2. Electrical characteristics

Item	Specification	Test conditions
Inductance	76.0mH ± 25% (TEMP.)	●EQUIP. : LCR METER (Agilent 4284A)
		or equivalent
		●FREQ. : 1 kHz
		●LEVEL : 0.6 mA
Core loss	T. B. D max. (at 100°C)	●COIL : φ0.4 2UEW 100Ts
		●EQUIP. : B-H ANALYZER
		●FREQ. : 25 kHz
		●LEVEL : 200 mT
		●COIL : N1=N2=5Ts
		●MEASURE MODE: Z+Z

3. Marking

"PC47 TDK Number Z"-mark are marked with paint on the cores.

4. The core shall be supplied as pairs.

5. Manufacturing site

China: TDK Xiamen Co., Ltd

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Inspection Test Schedule

Magnetics Business Group of TDK CORPORATION
 performs outgoing inspection tests on the inspection items in Table-1.
 The inspection shall be performed according to ISO-2859-1
 Inspection level I (n=10, c=0 for dimension and n=5, c=0 for core loss)

Table-1. AQL and Inspection items

	AQL	Inspection Items
A	0.4 %	Inductance and Crack
B	1.0 %	Dimensions
C	1.5 %	Chipping

Permissible limit of chips are prescribed as follows :

- (1) Depth of a permissible chip shall be 1.0 mm max. on mating faces and 1.2 mm max. on other faces. Chips on the corner should be judged by Table-2.
- (2) For the chips more than two on a core piece, the total area of chips shall be used as judgement.
- (3) Others.
 Not described above are based on IEC 60424 specification.

Table-2. Permissible limit of the area of chips.

	Class	
Mating face	1	(1 mm ²)
Except mating face	4	(4.0 mm ²)

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